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RESPONSE AFTER FINAL
EXPEDITED PROCEDURE
EXAMINING GROUP 2818

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Kuninori KAWABATA, et al.

Art Unit: 2818

Serial No.: 09/994,610

Examiner: L. Pham

Filed: November 28, 2001

Docket No.: 100353-00086

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

RESPONSE UNDER 37 C.F.R. § 1.121

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 5, 2004
(January 3 – Saturday)

Sir:

In reply to the Office Action mailed October 3, 2003, please amend the above-identified application as follows: